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1 Designing RNA structures: natural and artificial selection 89%


Barry Cohen , Steven Skiena

Proceedings of the sixth annual international conference on Computational biology

April 2002

Messenger RNA (mRNA) sequences serve as templates for proteins according to the triplet code, in which each of the $4^3 = 64$ different codons (sequences of three consecutive nucleotide bases) in RNA either terminate transcription or map to one of the 20 different amino acids (or residues) which build up proteins. Because there are more codons than residues, there is inherent redundancy in the coding. Certain residues (e.g. tryptophan) have only a single corresponding codon, while other ...

2 Algebraic perturbative calculations in high energy physics: Methods, 87%


algorithms, computer programs and physical applications

Levan R. Surguladze , Mark A. Samuel








Proceedings of the 1991 international symposium on Symbolic and algebraic computation June 1991
3 A computer architecture for dynamic finite element analysis 87%


L. M. Napolitano

ACM SIGARCH Computer Architecture News , Proceedings of the 13th annual international symposium on Computer architecture June 1986

Volume 14 Issue 2

The explicit finite element method for dynamic structural analysis can be expressed in a highly parallel form. A proposed architecture is described that addresses the fundamental algorithmic issues of the method. An approximation equation for the efficiency is given as a function of processing time for a single element, the fundamental interprocessor communication time, the number of processors, and the number of finite elements. The predicted speedups are plotted against postulated parallel ...

- 4** Delinearization: an efficient way to break multiloop dependence equations 85%
 Vadim Maslov
ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1992 conference on Programming language design and implementation July 1992
Volume 27 Issue 7
Exact and efficient data dependence testing is a key to success of loop-parallelizing compiler for computationally intensive programs. A number of algorithms has been created to test array references contained in parameter loops for dependence but most of them are unable to answer the following question correctly: Are references $C(i1 + 10j1)$ and $C(i2$
- 5** ProCEED: an expert system for multivariate process control systems design 82%
 V. K. Chawla , R. K. Ragade , P. B. Deshpande
Proceedings of the first international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 June 1988
Design and implementation of effective control systems is vital to the competitiveness of the process industry. Process control system design requires knowledge based systems which are capable of efficient numeric computing along with the symbolic reasoning. A brief survey presented in this paper shows that many industrial and academic research groups, are actively developing such knowledge based tools, in the U.S. and other countries. ProCEED, the expert system described in this ...
- 6** Integrated environment for intelligent control 82%
 Ming Rao , Tsung-Shann Jiang , Jeffrey J.-P. Tsai
Proceedings of the first international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 June 1988
- 7** Numerical Solution of Systems of Nonlinear Equations 80%
 Ferdinand Freudenstein , Bernhard Roth
Journal of the ACM (JACM) October 1963
Volume 10 Issue 4
- 8** Constraint-based array dependence analysis 80%
 William Pugh , David Wonnacott
ACM Transactions on Programming Languages and Systems (TOPLAS) May 1998
Volume 20 Issue 3
Traditional array dependence analysis, which detects potential memory aliasing of array references is a key analysis technique for automatic parallelization. Recent studies of benchmark codes indicate that limitations of analysis cause many compilers to overlook large amounts of potential parallelism, and that exploiting this parallelism requires algorithms to answer new question about array references, not just get better answers to the old questions of aliasing. We need to ask about the ...
- 9** Detecting coarse-grain parallelism using an interprocedural parallelizing 80%
 compiler
Mary H. Hall , Saman P. Amarasinghe , Brian R. Murphy , Shih-Wei Liao , Monica S. Lam
Proceedings of the 1995 ACM/IEEE conference on Supercomputing (CDROM) December 1995
- 10** Static analysis of upper and lower bounds on dependences and parallelism 80%
 William Pugh , David Wonnacott
ACM Transactions on Programming Languages and Systems (TOPLAS) July 1994

Volume 16 Issue 4

Existing compilers often fail to parallelize sequential code, even when a program can be manually transformed into parallel form by a sequence of well-understood transformations (as in the case for many of the Perfect Club Benchmark programs). These failures can occur for several reasons: the code transformations implemented in the compiler may not be sufficient to produce parallel code, the compiler may not find the proper sequence of transformations, or the compiler may not be able to pro ...

11 Beyond induction variables

80%



Michael Wolfe

ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1992 conference on Programming language design and implementation July 1992

Volume 27 Issue 7

Induction variable detection is usually closely tied to the strength reduction optimization. This paper studies induction variable analysis from a different perspective, that of finding induction variables for data dependence analysis. While classical induction variable analysis techniques have been used successfully up to now, we have found a simple algorithm based on the Static Single Assignment form of a program that finds all linear induction variables in a loop. Moreover, this algorithm ...

12 Session 18: dependence analysis/loop parallelization: The range test: a dependence test for symbolic, non-linear expressions

80%



William Blume , Rudolf Eigenmann

Proceedings of the 1994 ACM/IEEE conference on Supercomputing November 1994

Most current data dependence tests cannot handle loop bounds or array subscripts that are symbolic, nonlinear expressions (e.g. $A(n*i+j)$, where $0 \leq j \leq n$). In this paper, we describe a dependence test, called the range test, that can handle such expressions. Briefly, the range test proves independence by determining whether certain symbolic inequalities hold for a permutation of the loop nest. Powerful symbolic analyses and constraint propagation techniques were developed to prove such in ...

13 A computer-aided linkage analysis system

77%



F. Bitonti , D. W. Cooper , D. N. Frayne , H. H. Hansen

Proceedings of the SHARE design automation project January 1965

A significant portion of mechanical engineering effort is spent in the kinematic analysis of mechanisms such as gears, cams, and linkages. Although linkages present a more complex problem of analysis than most other basic mechanisms, they are widely used because of their reliability, speed, and force-transmission properties. Engineers continuously seek improvements in existing linkages and devise linkages for new mechanical systems. Linkage analyses have traditionally been performed on the ...

14 Computer-aided design of nonlinear dynamic systems

77%




M. A. Murray-Lasso , Steiner Espestøyl

Proceedings of the June 1970 design automation workshop on Design automation June 1970

With the advent of large memory, fast digital computers with convenient input-output devices and for which high-level problem-oriented languages are available in a time-shared environment, it is becoming increasingly desirable to develop digital computer programs for the design of complex control systems. For some time the analog computer has been used for this purpose. In this paper a large digital computer program, called OLDS (On Line dynamic system


77%

15 Better tiling and array contraction for compiling scientific programs Geoff Pike , Paul N. Hilfinger**Proceedings of the 2002 ACM/IEEE conference on Supercomputing** November 2002

Scientific programs often include multiple loops over the same data; interleaving parts of different loops may greatly improve performance. We exploit this in a compiler for Titanium, a dialect of Java. Our compiler combines reordering optimizations such as loop fusion and tiling with storage optimizations such as array contraction (eliminating or reducing the size of temporary arrays). The programmers we have in mind are willing to spend some time tuning their code and their compiler parameters. ...

16 Decentralized priority control in data communication


77%

 L. Nisnevich , E. Strasbourger**ACM SIGARCH Computer Architecture News , Proceedings of the 2nd annual symposium on Computer architecture** December 1974

Volume 3 Issue 4

17 The dynamics of software project scheduling

77%

 Tarek K. Abdel-Hamid , Stuart E. Madnick**Communications of the ACM** May 1983

Volume 26 Issue 5

Software project scheduling is one of the major problem areas faced by software project managers today. While several quantitative software project resource and schedule estimation methods have been developed, such techniques raise some important, but as yet unresolved, dynamic issues. A systems dynamics (SD) approach is used to analyze several key dynamic software project scheduling issues.

18 A compiler technique for improving whole-program locality

77%


 Mahmut Taylan Kandemir**ACM SIGPLAN Notices , Proceedings of the 28th ACM SIGPLAN-SIGACT symposium on Principles of programming languages** January 2001

Volume 36 Issue 3

Exploiting spatial and temporal locality is essential for obtaining high performance on modern computers. Writing programs that exhibit high locality of reference is difficult and error-prone. Compiler researchers have developed loop transformations that allow the conversion of programs to exploit locality. Recently, transformations that change the memory layouts of multi-dimensional arrays---called data transformations---have been proposed. Unfortunately, both data and loop transformations have ...

19 A control-theoretic approach to the design of an explicit rate controller for

77%

 ABR service


Aleksandar Kolarov , G. Ramamurthy

IEEE/ACM Transactions on Networking (TON) October 1999

Volume 7 Issue 5

20 RS -Machines with Almost Blank Tape

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 C. C. Elgot , J. D. Rutledge**Journal of the ACM (JACM)** July 1964

Volume 11 Issue 3

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




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